

FIG. 1

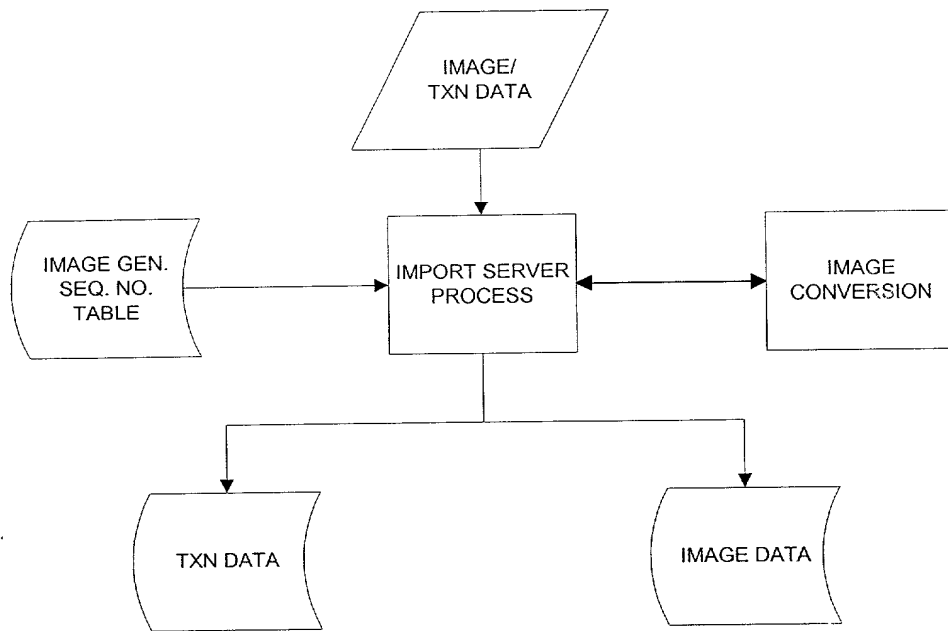


FIG. 2

```

graph TD
    102[RECEIVER] --> 104[(IMAGE ECE TXN DATA)]
    102 --> 106[(IMPORT CONTROL)]
    104 --> 108[IMAGE ENTRY PREPROCESSOR]
    106 --> 108
    110[(IMAGE ENTRY DATA)] --> 108
    108 --> 112[ENTRY FORMATTER PROCESS]
    112 --> 40[CROSS-REF LOAD PROCESS]
    112 --> 114[(FORMATTED IMAGE DATA)]
    40 --> 45[(CROSS-REF DATA)]
    114 --> 116[CPCS STRING LOAD PROCESS]
    116 --> 118[(I-STRING)]
    118 --> 120[CPCS OLRR]
    120 --> 122[DISTRIBUTION]
    122 --> 124[(REJECT D-STRING)]
    124 --> 126[STRING EXTRACTS]
    126 --> 128[INITIATE IMAGE REJECT REPAIR]
    118 --> 130[STRING EXTRACTS]
    130 --> 132[HFID CREATION]
    132 --> 134[(HFID)]
  
```

FIG. 3

FIG. 3

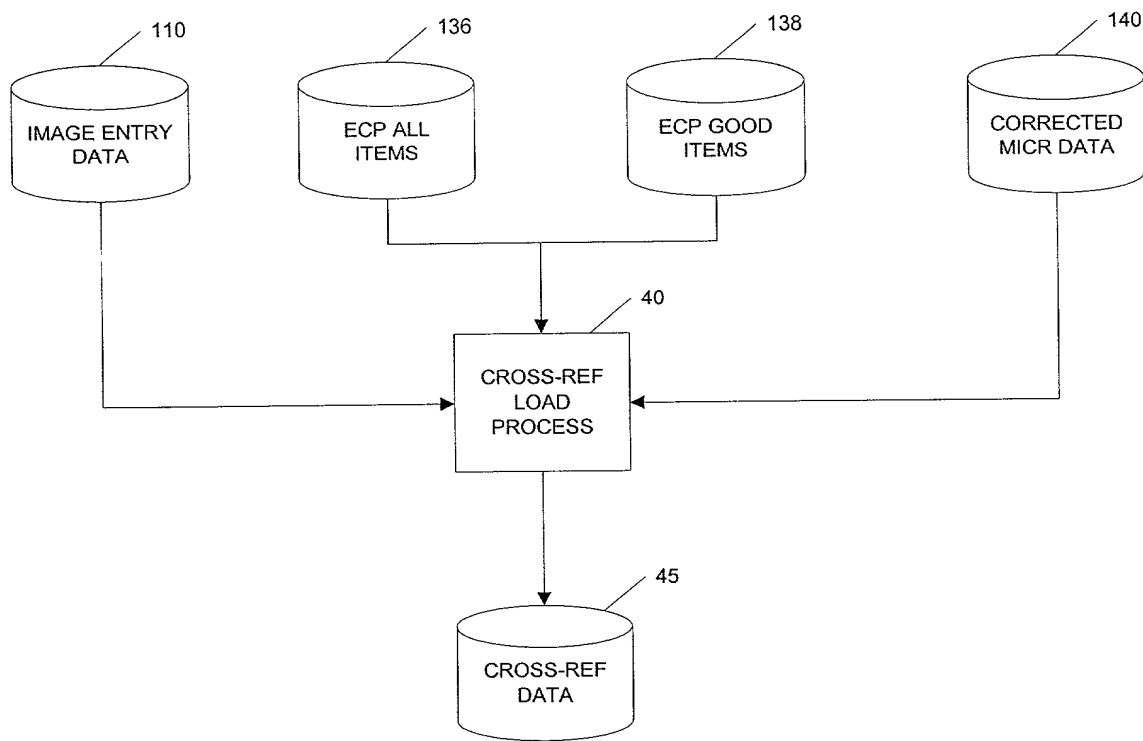


FIG. 4

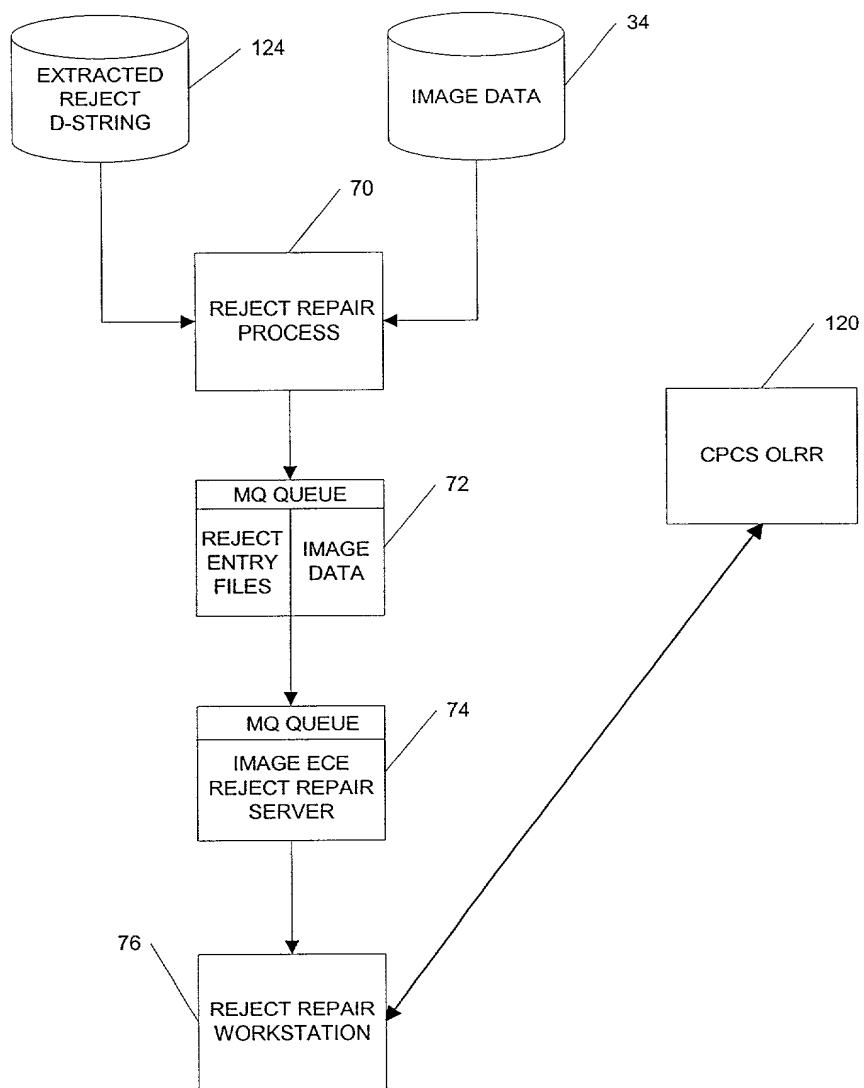


FIG. 5

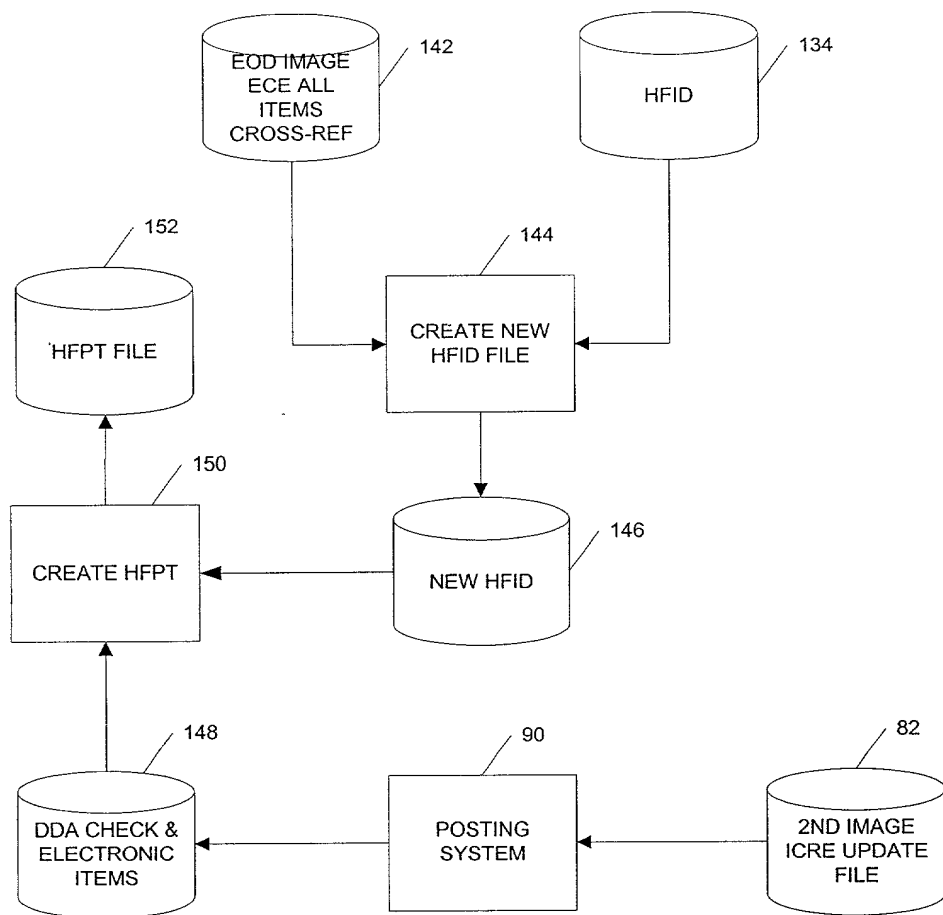


FIG. 6

1006400-030400

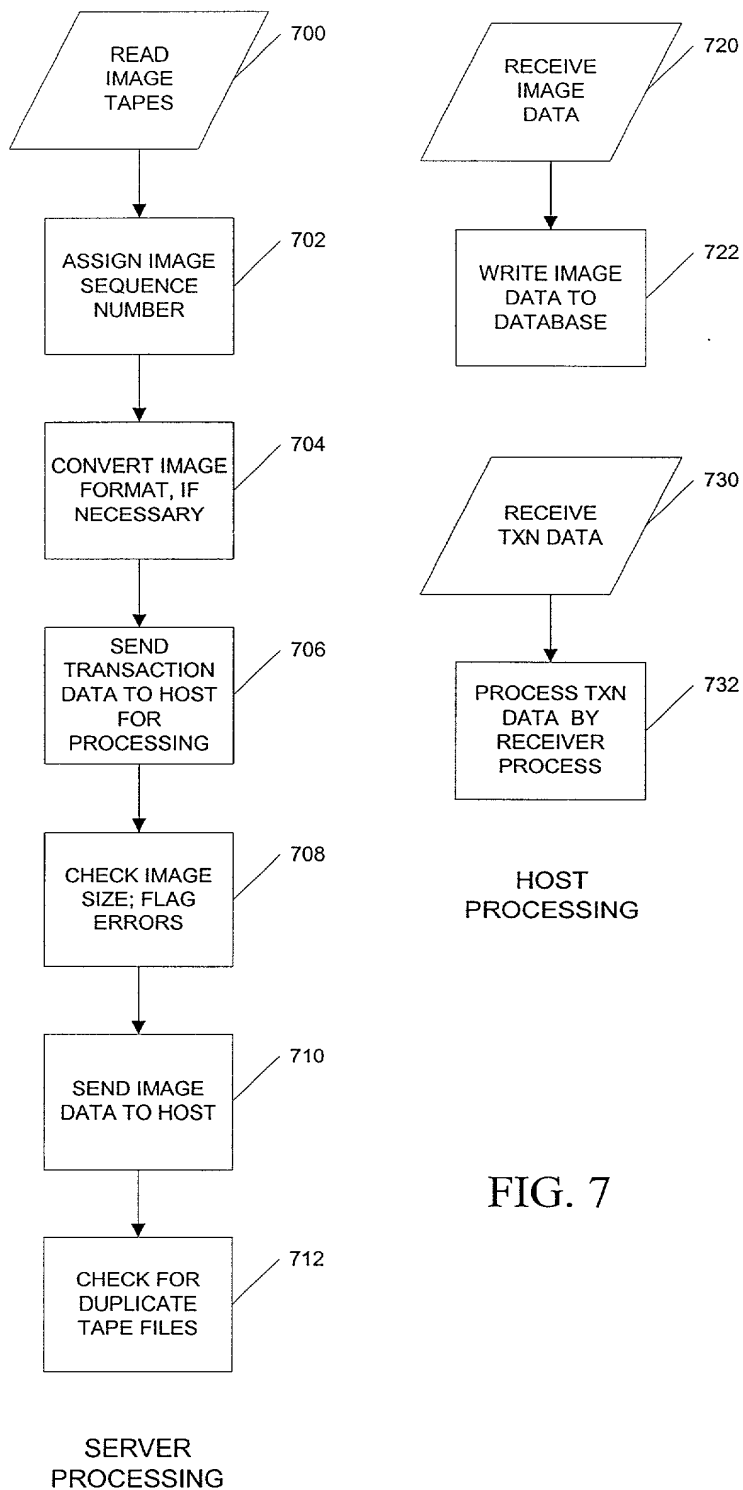


FIG. 7

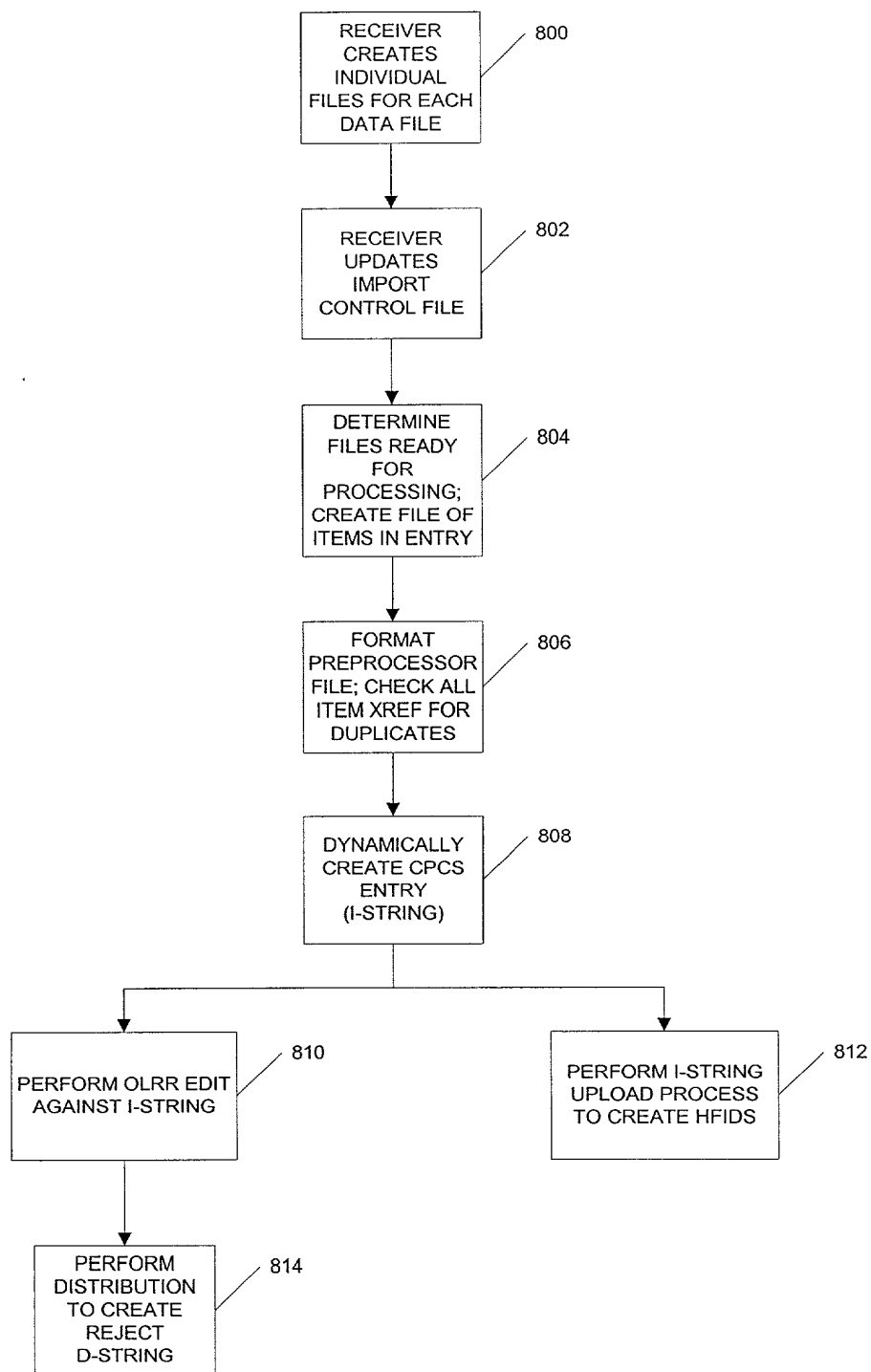


FIG. 8

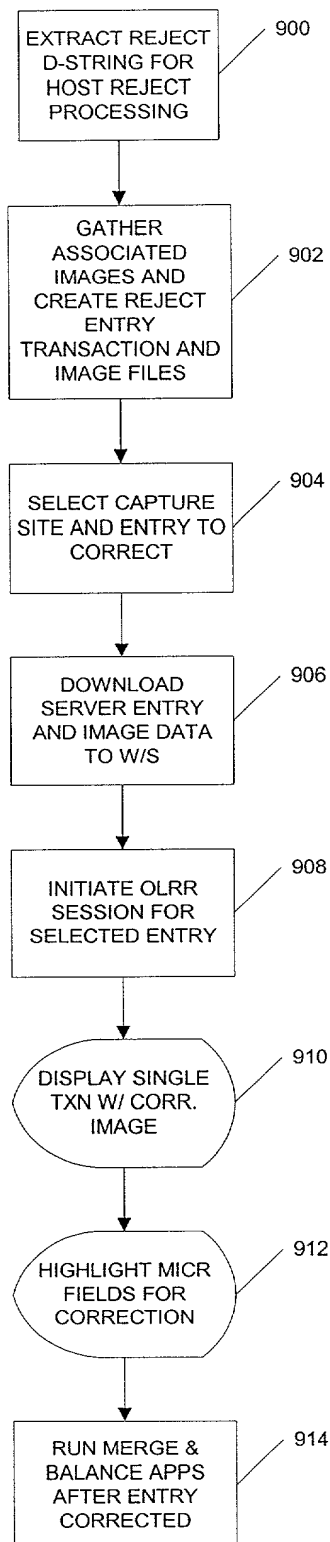


FIG. 9

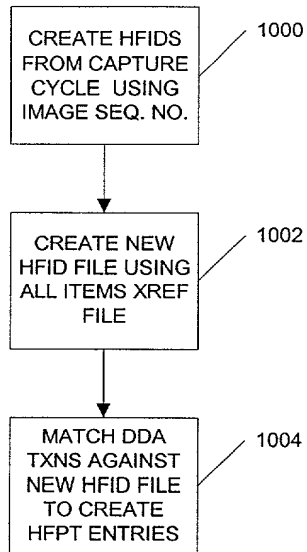


FIG. 10